



PATENT
Docket No. SPLX.P0007

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Mani Adell

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.: 10/043,853

Filing Date: 1/11/2002

For: INTEGRATED CIRCUIT WIRING
ARCHITECTURES THAT DEPOSE WIRES IN
A DIAGONAL AND MANHATTAN
DIRECTION

**INFORMATION DISCLOSURE
STATEMENT UNDER 37 C.F.R. § 1.97**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to

the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

This Information Disclosure Statement is submitted after receipt of a first Office Action on the merits but before the Final Office Action. Accordingly, a fee is required. However, Applicants note that on this day, July 22, 2004, Applicants have submitted another Information Disclosure Statement in electronic form and paid the requisite fee along with this electronic submission. See attached hard copy of electronic IDS and its fee transmittal. Therefore, Applicants submit that no separate fee is required for this Information Disclosure Statement.

However, in the unlikely event that the Patent Office determines that additional fees, extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 50-1128** referencing SPLX.P0007. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: July 22, 2004

Respectfully submitted,

By: _____


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Registration No. 39,585

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ELECTRONIC INFORMATION DISCLOSURE STATEMENT
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Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Simulating diagonal wiring directions using manhattan directional wires																																																																																																								
<div style="display: flex; justify-content: space-between;"> <div> <p>Application Number: 10/043853</p> <p>Confirmation Number: 9148</p> <p>First Named Applicant: Steven Teig</p> <p>Attorney Docket Number: SPLX.P0007</p> <p>Art Unit: 2826</p> <p>Examiner: Alexander O. Williams</p> <p>Search string: (5640327 or 5689433 or 5801385 or 5811863 or 5880969 or 5889329 or 6111756 or 6260183 or 6263475 or 6301686 or 6324674 or 6412097 or 6645842 or 20010009031).pn.</p> </div> <div style="text-align: right;"> <p>*10/043853*</p> </div> </div> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th>init</th> <th>Cite.No.</th> <th>Patent No.</th> <th>Date</th> <th>Patentee</th> <th>Kind</th> <th>Class</th> <th>Subclass</th> </tr> </thead> <tbody> <tr><td></td><td>1</td><td>5640327</td><td>1997-06-17</td><td>Ting</td><td></td><td></td><td></td></tr> <tr><td></td><td>2</td><td>5689433</td><td>1997-11-18</td><td>Edwards</td><td></td><td></td><td></td></tr> <tr><td></td><td>3</td><td>5801385</td><td>1998-09-01</td><td>Endo et al.</td><td></td><td></td><td></td></tr> <tr><td></td><td>4</td><td>5811863</td><td>1998-09-22</td><td>Rostoker et al.</td><td></td><td></td><td></td></tr> <tr><td></td><td>5</td><td>5880969</td><td>1999-03-09</td><td>Hama et al.</td><td></td><td></td><td></td></tr> <tr><td></td><td>6</td><td>5889329</td><td>1999-03-30</td><td>Rostoker et al.</td><td></td><td></td><td></td></tr> <tr><td></td><td>7</td><td>6111756</td><td>2000-08-29</td><td>Moresco</td><td></td><td></td><td></td></tr> <tr><td></td><td>8</td><td>6260183</td><td>2001-07-10</td><td>Raspopovic et al.</td><td>B1</td><td></td><td></td></tr> <tr><td></td><td>9</td><td>6263475</td><td>2001-07-17</td><td>Toyonaga et al.</td><td>B1</td><td></td><td></td></tr> <tr><td></td><td>10</td><td>6301686</td><td>2001-10-09</td><td>Kikuchi et al.</td><td>B1</td><td></td><td></td></tr> <tr><td></td><td>11</td><td>6324674</td><td>2001-11-27</td><td>Andreev et al.</td><td>B2</td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </tbody> </table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	5640327	1997-06-17	Ting					2	5689433	1997-11-18	Edwards					3	5801385	1998-09-01	Endo et al.					4	5811863	1998-09-22	Rostoker et al.					5	5880969	1999-03-09	Hama et al.					6	5889329	1999-03-30	Rostoker et al.					7	6111756	2000-08-29	Moresco					8	6260183	2001-07-10	Raspopovic et al.	B1				9	6263475	2001-07-17	Toyonaga et al.	B1				10	6301686	2001-10-09	Kikuchi et al.	B1				11	6324674	2001-11-27	Andreev et al.	B2										
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	12	6412097	2002-06-25	Kikuchi et al.	B1
	13	6645842	2003-11-11	Igarashi et al.	B2

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20010009031	2001-07-19	Nitta et al.	A1		

Signature

Examiner Name	Date

FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

Title of Invention	Simulating diagonal wiring directions using manhattan directional wires								
Application Number: 10/043853 *10/043853*									
Date: 2002-01-11									
First Named Applicant: Steven Teig									
Attorney Docket Number: SPLX.P0007									
Art Unit: 2826									
Examiner: Alexander O. Williams									
TOTAL FEE AUTHORIZED \$180									
Patent fees are subject to annual revisions on or about October 1st of each year.									
BASIC FILING FEE									
<table border="1"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Submission Of Information Disclosure Stmt Fee</td><td>1806</td><td>180</td><td>180</td></tr></tbody></table>		Fee Description	Fee Code	Amount \$	Fee Paid \$	Submission Of Information Disclosure Stmt Fee	1806	180	180
Fee Description	Fee Code	Amount \$	Fee Paid \$						
Submission Of Information Disclosure Stmt Fee	1806	180	180						
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/043,853
				Filing Date	1/11/2002
				First Named Inventor	Steven Teig
				Group Art Unit	2826
				Examiner Name	Alexander O. Williams
Sheet	1	of	1	Attorney Docket Number	SPLX.P0007

FOREIGN PATENT DOCUMENTS								
Examiner* Initials	Cite No. ¹	Foreign Patent Document			Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code (if known) ⁵				
	1.	JP	04-000677		01-06-1992	Fujiwara et al.	with English translation of Abstract;	
	2.	JP	2000-082743		03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓

NON PATENT LITERATURE DOCUMENTS				
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ⁶
	3.	Chen et al., Optimal Algorithms for Bubble Sort Based Non-Manhattan Channel Routing, May 1994, Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions Volume: 13 Issues, pp. 603-609.		
	4.	Cong J. et al., DUNE – A Multilayer Gridless Routing System, May 2001, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 20, iss. 5, pp. 633-647.		
	5.	Dion J. et al., Contour: A Tile-based Gridless Router, March 1995, Digital Western Research Laboratory, research Report 95/3, pp.1-22		
	6.	Merriam-Webster's Collegiate Dictionary, 10 th edition, Merriam-Webster Incorporated, p.606		
	7.	Morton, P. B. et al., An Efficient Sequential Quadratic Programming Formulation of Optimal Wire Spacing for Cross-Talk Noise Avoidance Routing, UCSC-CRL-99-05, March 10, 1999.		
	8.	NN71091316, Use of Relatively Diagonal And Rectangular Wiring Planes n Multilayer Packages, September 1971, IBM Technical Disclosure Bulletin, Vol. No. 14, Issue No. 4, pp. 1316-1317.		
	9.	Royle, J. et al., Geometric Compaction in One Dimension for Channel Routing, 24 th ACM/IEEE Design Automation Conference, 1987, pp 140-145.		
	10.	Tseng H. et al., A Gridless Multilayer Router for Standard Cell Circuits Using CTM Cells, Oct. 1999, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 18, iss. 10, pp. 1462-1479.		

Examiner Signature		Date Considered	
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number(optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/043,853
	Filing Date	1/11/2002
	First Named Inventor	Steven Teig
	Group Art Unit	2826
	Examiner Name	Alexander O. Williams
Total Number of Pages in This Submission	Attorney Docket Number	SPLX.P0007

Enclosures (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declarations(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Documents <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosures (Listed below): Postcard 10 cited references Copy of eIDS with a Fee Transmittal
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Date	July 22, 2004

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Typed or printed name	Mani Adeli		
Signature		Date	July 22, 2004

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